Micron Taiwan - DEG

Accelerating the transformation of information to intelligence



Intelligence Accelerated

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Micron Profile

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Micron in Taiwan





TAIPEI Started in 1994

TAOYUAN Started in 2003



TAICHUNG Started in 2006

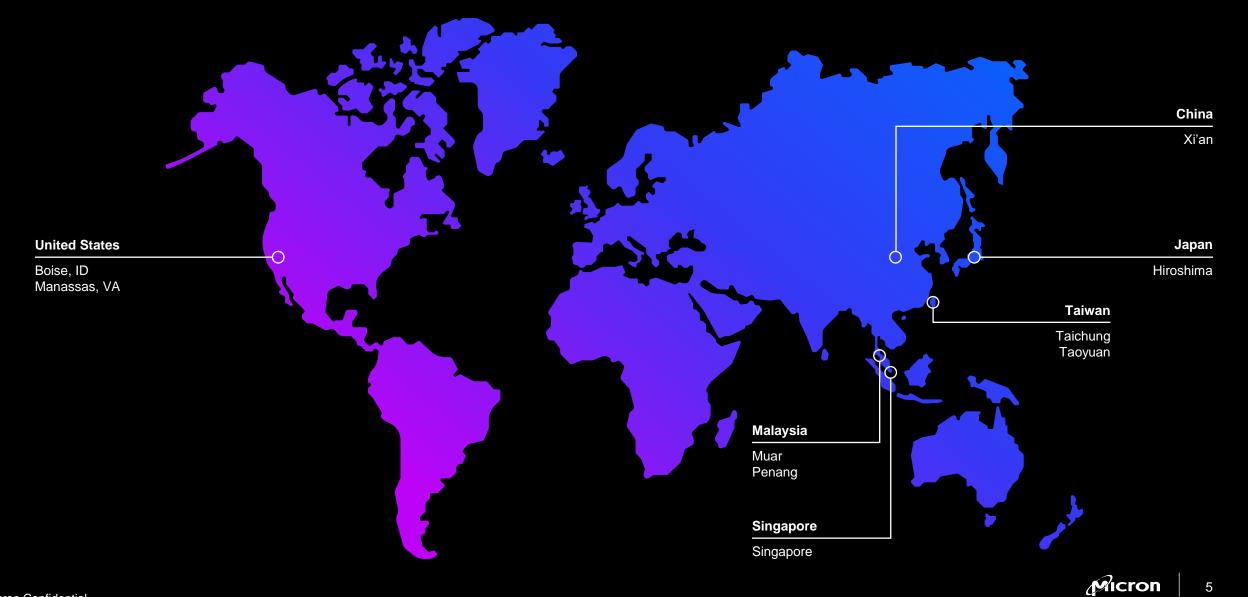


HSINCHU Started in 2020

~30 Years in Taiwan	10,000+ Team members		EUV In A3 fab		HBM R&D and Manufacturing	34% Diversity employees
DRAM Center of Excellence					Currently only in Taiwon	
1γ advanced pilot line corridor		Assembly & Test factory	DRAM Mega Fabs	1α 1β	Currently only in Taiwan 2023 Taiwan & Japan	7,500+
Advanced Packa High Bandwidth M		Product Engineering	Customer Labs	1 γ⊡	2025 with EUV	Team members participated volunteering activities in CY22

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Our Manufacturing Sites



Micron innovation: industry-leading memory and storage 45+ years | 55,500+ patents

Ç) 1984 ()1999 (⊋2006	\2017	Q2020 (2022
	Introduced world's smallest 256K DRAM	Shipped industry's first DDR memory	Delivered industry's densest server memory module (16GB)	Micron ships first string-stacked 3D NAND	Launched GDDR6X, first memory with PAM4 multi-level signaling Shipped industry's first 176-Layer NAND flash	Expanded 176-Layer NAND across TLC and QLC SSD portfolio Shipped industry's first 1β DRAM and 232-Layer NAND
Design world's 64K DI	ned Reached s smallest 1-megabit [DRAM 0F ² memory c architecture	, ,		stry's Shipped industry's first 1α DRAM process technology	fastest, highest-capacity

Shipped first to market 128GB high-capacity DDR5 RDIMMS

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Micron

competition to advance generative AI innovation

Why Join Micron

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Micron Taiwan – A Growing Company

Taiwan is an important production base for Micron DRAM, and Micron also continues to invest in Taiwan. Micron's Taiwan sites include sales and technical support in Taipei, along with two state-of-the-art fabrication facilities in Taichung and Taoyuan and a backend facility in Taichung. Together the Taichung and Taoyuan sites make up the company's High-Volume DRAM Center of Excellence.

台灣唯美光DRAM重要生產據點, 總部亦持續在台灣投資 美光科技在台灣的工廠包括位於台 北的銷售和技術協助,以及位於台 中和桃園的兩個最先進的製造工廠 以及位於台中的後端工廠。台中和 桃園工廠共同構成了該公司的大容 量 DRAM 卓越中心。



Micron Taiwan – Great Place to work

<u>Culture</u>



倡導多元平等包容的 DEI 文化 Diversity, Equity, Inclusion Culture

- 美光基金會在台透過 Girls Going Tech 活 動積極鼓勵女性青年學子投身科技業,自 2018年起 足跡遍布北/中台灣主要女高校, 與學生分享美光女性科技人的職涯發展歷程
- 設ERG員工資源團體,包含:<u>Capable</u>, <u>Micron Women's Leadership Network</u> (MWLN), <u>Micron Young Professionals</u> (MYP), <u>PRIDE+Allies</u>, <u>Mosaic</u>

以人為本 People oriented

- 1) Work-Life balance
- 2) 混和辦公 WFH 1 day per week
- 3) 彈性上下班 Flexible work time
- 4) 80小時帶薪病假 80 hours paid leave

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Career and Development



新人夥伴制度 Launch Buddy

職涯導師計畫 Mentor Program

進修教育計畫 Academic Advancement Program

美光大學 - 線上課程 Micron University - e-learning Portal

內部多元發展職涯機會 Internal Job Application Programs

Pay and Incentives



Competitive pay in the industry 半導體業具有競爭力的薪資

Incentive Pay Plan 年度績效獎金

Employee Stock Purchase Plan ESPP 員工購股計畫

Micron



DRAM Engineering Group - DEG

1. Working in DEG

2. Job Opportunities - DRAM Product Engineer

3. DRAM Product Engineer Partnerships



Working in DEG

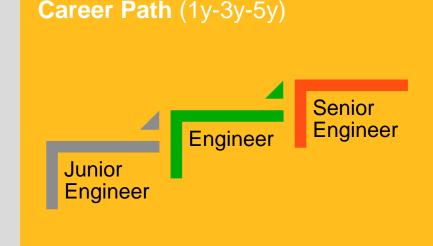
Overall Taiwan PE Team Mission and Vision

Vision:

Establishing execution excellence throughout the life of our DRAM products

Mission:

- 1. Be the center of excellence for high volume DRAM with the advantage of proximity to Micron manufacturing base
- 2. Focus on ramping and sustaining yield and quality to deliver DRAM product at the quickest turnaround time



Who we are looking for...

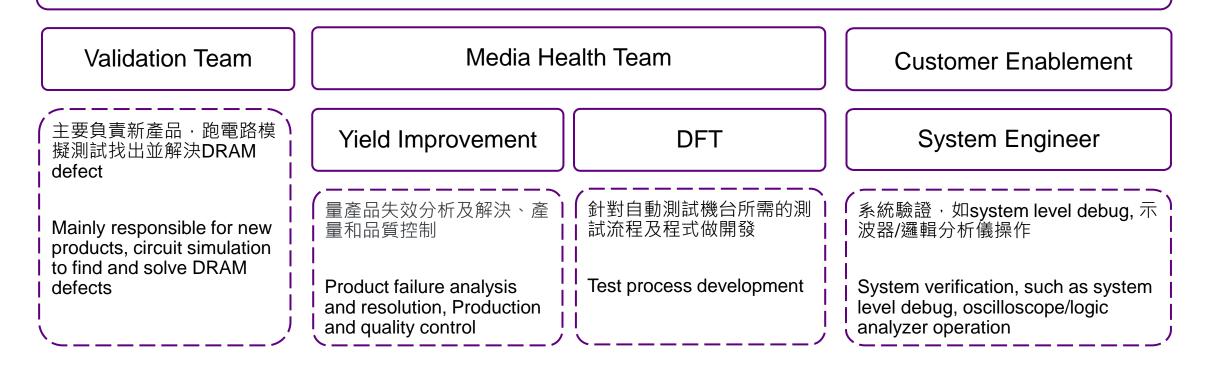
- 1. Exceptional English communication skills
- 2. Embrace the opportunity to collaborate with different countries and departments
- 3. Learned DRAM, Digital Design, Circuit, VLSI, or Programming
- 4. Creative problem-solving

Training Program (30d-60d-90d)

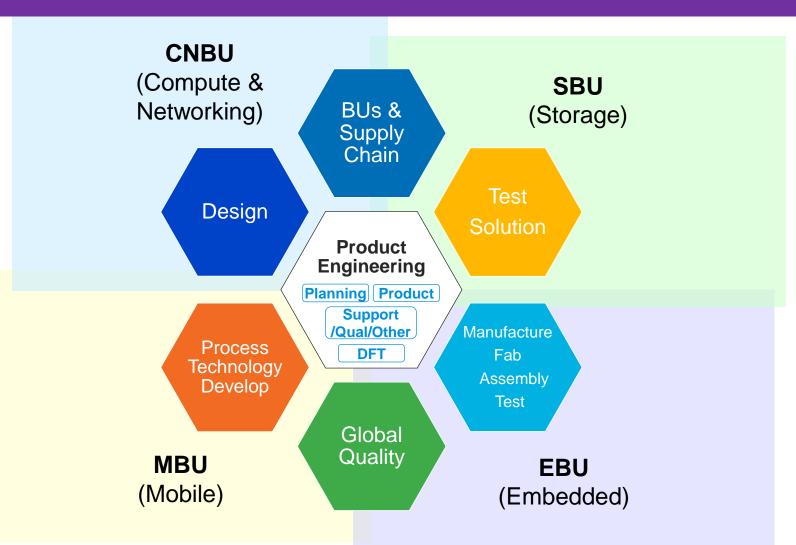
- First 30 Days: Orientation and Foundation Building
- 30 60 Days: Skill Development and Integration
- 60 90 Days: Autonomy and On-the-Job Training

Job Opportunities - DRAM Product Engineer

Product Engineering Team



DRAM Product Engineer Partnerships

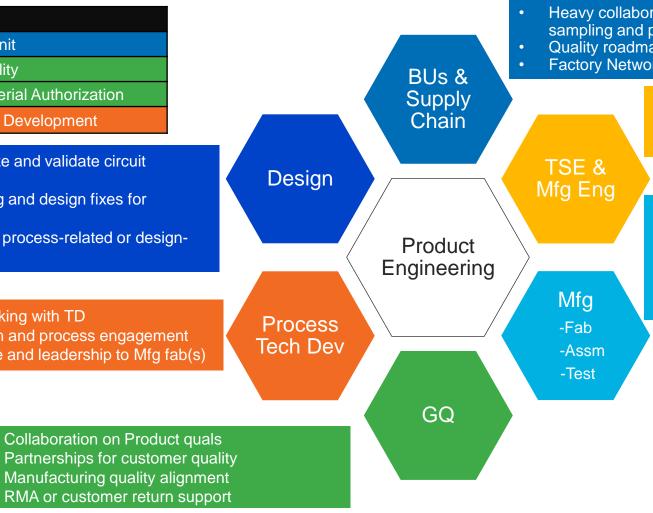


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Product Engineer Partnerships - Functions

Acronyms	
BU	Business Unit
GQ	Global Quality
RMA	Return Material Authorization
TD	Technology Development

- Simulate, characterize and validate circuit behavior
- Collaborate on debug and design fixes for • performance issues
- Determine if fails are process-related or designrelated
- Specific teams working with TD
- Heavy alpha design and process engagement
- Transfer assistance and leadership to Mfg fab(s)



- Heavy collaboration on time to market, customer sampling and product readiness Quality roadmap partnership
- - Factory Network guidance
 - Collaboration on Test flow definitions
 - TTR roadmaps
 - Ongoing test coverage and SPC partnerships
 - Backend yield analysis and EFA support for SWRs, conversion and deviation
 - SPC/SBL collaboration and Dispo recommendation for Excursions, Conversions, QAWRs with MQA and Site quality team
 - IE help to prioritize critical lots

Acronyms	
Assm	Assembly
EFA	Electrical Failure Analysis
IE	Industrial Engineer
Mfg	Manufacturing Group
MQA	Manufacturing Quality Assurance
QAWR	Quality Assurance Work Report
SBL	Statistical Bin Limits
SPC	Statistical Process Control
SWR	Special Work Request
TTR	Test Time Reduction

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2023 Mid-autumn Festival BBQ

2022 Year-end Party

Vibrancy and Fun in DEG

2023 Year-end Party

Volunteering

Basketball club